



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2773

| | | | | |
|---|---|-----------------------------------|--|---|
| SERIAL NUMBER 09/878,681 | FILING DATE 06/11/2001 RULE | CLASS 257 | GROUP ART UNIT 2811 | ATTORNEY DOCKET NO. FIS9-1997-0163B- US4 |
| APPLICANTS Ronald J. Bolam, E. Fairfield, VT; Subhash B. Kulkarni, Cortlandt Manor, NY; Dominic J. Schepis, Wappinger Falls, NY; | | | | |
| ** CONTINUING DATA ***** THIS APPLICATION IS A CIP OF 09/859,146 05/16/2001 WHICH IS A CON OF 09/148,918 09/04/1998 PAT 6,281,095 WHICH IS A DIV OF 09/009,445 01/20/1998 PAT 6,133,610 | | | | |
| ** FOREIGN APPLICATIONS ***** | | | | |
| IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 07/18/2001 | | | | |
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <u>Examiner's Signature</u> <u>Initials</u> | | STATE OR COUNTRY VT | SHEETS DRAWING 15 | TOTAL CLAIMS 38 |
| | | | | INDEPENDENT CLAIMS 7 |
| ADDRESS Ratner & Prestia One Westlakes, Berwyn, Suite 301 P.O. Box 980 Valley Forge, PA 19482-0980 | | | | |
| TITLE Silicon-on-insulator chip having an isolation barrier for reliability | | | | |
| FILING FEE RECEIVED 1484 | FEES: Authority has been given in Paper No. _____ to charge/CREDIT DEPOSIT ACCOUNT No. _____ for following: | | <input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit | |